Port allocation, JED AVR256 single board computer						
Port	**Pin**	**Net @ CPU**	**Pin function**	**Main use**	**Connection**	**Alternate use**
PA0	78	AD0	AD0	Kb1	J26-12	RAM AD0
PA1	77	AD1	AD1	Kb2	J26-11	RAM AD1
PA2	76	AD2	AD2	Kb3	J26-10	RAM AD2
PA3	75	AD3	AD3	Kb4	J26-9	RAM AD3
PA4	74	AD4	AD4	Kb5	J26-8	RAM AD4
PA5	73	AD5	AD5	Kb6	J26-7	RAM AD5
PA6	72	AD6	AD6	Kb7	J26-6	RAM AD6
PA7	71	AD7	AD7	Kb8	J26-5	RAM AD7
PB0	19	MMCSDCS	SS*/PCINT0/T0	PC INT0		ACCELEROMETER
PB1	20	SCK	SCK/PCINT1T1	SCK		PLUS DATA FLASH
PB2	21	MOSI	MOSI/PCINT2	MOSI		PLUS ISP PROG SKT
PB3	22	MISO	MISO/PCINT3	MISO		PLUS SD MEMORY SKT
PB4	23	DB-CS0	OC2A/PCINT4	UPSTAIRS CS	J28-11	
PB5	24	DB-CS1	OC1A/PCINT5	UPSTAIRS CS	J28-12	
PB6	25	DB-CS2	OC1B/PCINT6	UPSTAIRS CS	J28-13	
PB7	26	DB-CS3	OC0A/OC1C/PCINT7	UPSTAIRS CS	J28-14	
PC0	53	A8	A8	RAM A8		
PC1	54	A9	A9	RAM A9		
PC2	55	A10	A10	RAM A10		
PC3	56	A11	A11	RAM A11		
PC4	57	A12	A12	RAM A12		
PC5	58	A13	A13	RAM A13		
PC6	59	A14	A14	RAM A14		
PC7	60	A15	A15	RAM A15		
PD0	43	SCL	SCL/INT0	I2C SCL		RTC PLUS FRAM
PD1	44	SDA	SDA/INT1	I2C SDA		PLUS I2C UPSTAIRS
PD2	45	RX_1	RXD1/INT2	RX SERIAL 1		GPS TX PLUS XPORT OUT
PD3	46	TX_1	TXD1/INT3	TX SERIAL 1		GPS RX PLUS XPORT IN
PD4	47	PD4	ICP1	ICP1	J7-1	
PD5	48	PD5	XCK1	ONE WIRE		
PD6	49	PD6	T1	T1	J6-2	
PD7	50	PD7	T0	T0	J6-1	
PE0	2	RX_0	RXD0/PCINT8	RX SERIAL 0	J39-3	RADIO OPT RX PLUS 80211 TX
PE1	3	TX_0	TXD0	TX SERIAL 0	J39-5	RADIO OPT TX PLUS 80211 RX
PE2	4	PE2	XCK0/AIN0	AIN0	J8-1	

PE3	5	PE3	OC3A/AIN1	OC3A	J8-2	
PE4	6	PE4	OC3B/INT4	OC3B	J8-3	
PE5	7	PE5	OC3C/INT5	OC3C	J8-4	
PE6	8	CARD_DETECT	T3/INT6	SD MEM SKT	J29-3	T3/INT6 (J29-3)
PE7	9	PE7	CLKO/ICP3/INT7	IPC3	J7-2	
PF0	97	ADC0	ADC0	ADC0	J5-4	
PF1	96	ADC1	ADC1	ADC1	J5-3	
PF2	95	ADC2	ADC2	ADC2	J5-2	
PF3	94	ADC3	ADC3	ADC3	J5-1	
PF4	93	ADC4	ADC4/TCK	ADC4	J4-4	
PF5	92	ADC5	ADC5/TMS	ADC5	J4-3	
PF6	91	ADC6	ADC6/TDO	ADC6	J4-2	
PF7	90	ADC7	ADC7/TDI	ADC7	J4-1	
PG0	51	WR*	WR*	WR*		BACKLIGHT LCD
PG1	52	RD*	RD*	RD*	J26-3	READ MEMORY PLUS Kb-10
PG2	70	ALE	ALE	Kb-9	J26-4	ALE MEMORY
PG3	28	WRITE_PROTECT	TOSC2	SD MEM DETECT	J29-2	
PG4	29	XPORT_RST*	TOSC1	XPORT RESET	U21-3	
PG5	1	DFCS*	OC0B	DATAFLASH CS		
PH0	12	RX_2	RXD2	RX SERIAL 2	J45-5/3	
PH1	13	TX_2	TXD2	TX SERIAL 2	J45-3/5	
PH2	14	ACC_CS*	XCK2	ACC_CS*		
PH3	15	PH3	OC4A	OC4A	J9-1	
PH4	16	PH4	OC4B	OC4B	J9-2	
PH5	17	PH5	OC4C	OC4C	J9-3	
PH6	18	PH6	OC2B	OC2B	J9-4	
PH7	27	PH7	T4	T4	J6-3	
PJ0	63	RX_3	RXD3/PCINT9	RX SERIAL 3	J44-3/5	MICRO USB TX
PJ1	64	TX_3	TXD3/PCINT10	TX SERIAL 3	J44-5/3	MICRO USB RX
PJ2	65	CTS_3	XCK3/PCINT11	CTS SERIAL 3	J44-4/6	
PJ3	66	RTS_3	PCINT12	RTS SERIAL 3	J44-6/4	
PJ4	67	CTS_2	PCINT13	CTS SERIAL 2	J45-6/4	
PJ5	68	RTS_2	PCINT14	RTS SERIAL 2	J45-4/6	
PJ6	69	CPU_IGN_ON	PCINT15			
PJ7	79	KEEP_ALIVE		PWR CONTROL		
PK0	89	ADC8	ADC8/PCINT16	ADC8	J3-4	
PK1	88	ADC9	ADC9/PCINT17	ADC9	J3-3	
PK2	87	ADC10	ADC10/PCINT18	ADC10	J3-2	
PK3	86	ADC11	ADC11/PCINT19	ADC11	J3-1	

PK4	85	ADC12	ADC12/PCINT20	ADC12	J2-4	
PK5	84	ADC13	ADC13/PCINT21	ADC13	J2-3	
PK6	83	ADC14	ADC14/PCINT22	ADC14	J2-2	
PK7	82	ADC15	ADC15/PCINT23	ADC15	J2-1	
PL0	35	PL0	ICP4	ICP4	J7-3	
PL1	36	PL1	ICP5	ICP5	J7-4	
PL2	37	PL2	T5	T5	J6-4	
PL3	38	PL3	OC5A	RTS SERIAL 0	J39-4	RADIO-OPT CTS (J50-7) PLUS 80211 CTS
PL4	39	PL4	OC5B	CTS SERIAL 0	J39-6	RADIO-OPT RTS (J50-8) PLUS 80211 RTS
PL5	40	DSR	OC5C	DSR SERIAL 0	J39-2	80211 DSR
PL6	41	CARRIER_DETECT		DCD SERIAL 0	J39-1	RADIO-OPT CD PLUS 80211 CD
PL7	42	DTR*		DTR SERIAL 0	J39-7	COMS ON' (INVERTED)
RESET	30	RESET*	RESET			
XTAL1	34	XTAL1	Crystal input			
XTAL2	33	XTAL2	Crystal output			
AREF	98	4.096V				
AVCC	100	VCC_A				
VCC	10 31	VCC				
	61 80	VCC				
GND	11 32 62	GND				
	81 99	GND				